



**PATENT APPLICATION**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of

Docket No: Q63945

Hiroki KOIKE

Appln. No.: 09/824,751

Confirmation No.: 9722

Group Art Unit: 2818

Filed: April 04, 2001

Examiner: Esaw T. Abraham

For: SEMICONDUCTOR MEMORY DEVICE AND TESTING SYSTEM AND TESTING METHOD

**INFORMATION DISCLOSURE STATEMENT**  
**UNDER 37 C.F.R. §§ 1.97 and 1.98**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure under 37 C.F.R. § 1.56, Applicant hereby notifies the U.S. Patent and Trademark Office of the documents which are listed on the attached PTO/SB/08 A & B (modified) form and listed herein and which the Examiner may deem material to patentability of the claims of the above-identified application.

1. Byung-Gil Jeon et al., "A novel cell charge evaluation scheme and rest method for 4 Mb nonvolatile ferroelectric RAM" ICVC '99. 6<sup>th</sup> INTERNATIONAL CONFERENCE ON VLSI AND CAD (CAT. NO. 99EX361), ICVC '99 6<sup>TH</sup> INTERNATIONAL CONFERENCE IN VLSI AND CAD, SEOUL, SOUTH KOREA, 26-27 OCT. 1999, pages 281-284, XP002271012 1999, Piscataway, NJ, USA IEEE, USA ASBN: 0-7803-5727-2.
2. Byung-Gil Jeon et al.: "A 0.4  $\mu$  , 3.3 V 1T1C 4 Mb nonvolatile ferroelectric RAM with fixed bit-line reference voltage scheme and data protection circuit", 2000 IEEE INTERNATIONAL SOLID-STATE CIRCUITS CONFERENCE.

# INFORMATION DISCLOSURE STATEMENT

U.S. Appln. No.: 09/824,751

DIGEST OF TECHNICAL PAPERS (CAT. NO. 00CH37056), 2000 IEEE INTERNATIONAL SOLID-STATE CIRCUITS CONFERENCE. DIGEST OF TECHNICAL PAPERS, SAN FRANCISCO, CA, USA, IEEE, USA ISBN: 0-7803-5853-8

3. U.S. Patent No. 5,661,690, issued August 26, 1997, previously submitted.
4. European Patent Publication No. 0 986 066, published March 15, 2000, previously submitted.
5. Choy J. et al., "Differential Sense Amplifier Reference: Current Mode for Production Screening", Motorola Technical Developments, Vol. 40, (January 2000), pp. 3-7, previously submitted.
6. European Patent Publication No. 0 400 184, published December 5, 1990, previously submitted.
7. European Patent Publication No. 0 953 989, published November 3, 1999, previously submitted.
8. Japanese Patent Publication No. 11-176195, published October 29, 1999, previously submitted.
9. United States Patent No. 5,675,546, issued October 7, 1997, previously submitted.

One copy of each of the listed documents is submitted herewith, except those indicated as being previously submitted. Also submitted herewith are European Search Reports dated March 4, 2004 and May 27, 2004 issued in a corresponding European Search patent application which cites such documents.

The present Information Disclosure Statement is being filed: (1) No later than three months from the application's filing date; (2) Before the mailing date of the first Office Action on the merits (whichever is later); or (3) Before the mailing date of the first Office Action after

INFORMATION DISCLOSURE STATEMENT

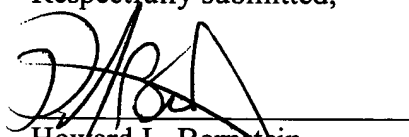
U.S. Appln. No.: 09/824,751

filing a request for continued examination (RCE) under §1.114, and therefore, no Statement under 37 C.F.R. § 1.97(e) or fee under 37 C.F.R. § 1.17(p) is required.

The submission of the listed documents is not intended as an admission that any such document constitutes prior art against the claims of the present application. Applicant does not waive any right to take any action that would be appropriate to antedate or otherwise remove any listed document as a competent reference against the claims of the present application.

The USPTO is directed and authorized to charge all required fees, except for the Issue Fee and the Publication Fee, to Deposit Account No. 19-4880. Please also credit any overpayments to said Deposit Account. A duplicate copy of this paper is attached.

Respectfully submitted,

  
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WASHINGTON OFFICE

**23373**

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Date: October 29, 2004

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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Sheet

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of

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*Complete if Known*

Application Number	09/824,751
Confirmation Number	9722
Filing Date	April 04, 2001
First Named Inventor	Hiroki KOIKE
Art Unit	2818
Examiner Name	Esaw T. Abraham
Attorney Docket Number	Q63945

OCT 29 2004

**U.S. PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document
		Number	Kind Code <sup>2</sup> (if known)		
		US			
		US			
		US			
		US			
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		US			
		US			
		US			
		US			

**FOREIGN PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document			Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Translation <sup>6</sup>
		Country Code <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)			

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city, and/or country where published.	Translation <sup>6</sup>
		Byung-Gil Jeon et al., "A novel cell charge evaluation scheme and rest method for 4 Mb nonvolatile ferroelectric RAM" ICVC '99. 6th INTERNATIONAL CONFERENCE ON VLSI AND CAD (CAT. NO. 99EX361), ICVC '99 6TH INTERNATIONAL CONFERENCE IN VLSI AND CAD, SEOUL, SOUTH KOREA, 26-27 OCT. 1999, pages 281-284, XP002271012 1999, Piscataway, NJ, USA IEEE, USA ASBN: 0-7803-5727-2.	
		Byung-Gil Jeon et al.: "A 0.4 $\mu$ m, 3.3 V 1T1C 4 Mb nonvolatile ferroelectric RAM with fixed bit-line reference voltage scheme and data protection circuit", 2000 IEEE INTERNATIONAL SOLID-STATE CIRCUITS CONFERENCE. DIGEST OF TECHNICAL PAPERS (CAT. NO. 00CH37056), 2000 IEEE INTERNATIONAL SOLID-STATE CIRCUITS CONFERENCE. DIGEST OF TECHNICAL PAPERS, SAN FRANCISCO, CA, USA, IEEE, USA ISBN: 0-7803-5853-8	

Examiner Signature	Date Considered
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup>Applicant's unique citation designation number (optional). <sup>2</sup>See Kind Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov), MPEP 901.04 or in the comment box of this document. <sup>3</sup>Enter Office that issued the document, by the two-letter code (WIPO Standard ST. 3). <sup>4</sup>For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup>Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup>Applicant is to indicate here if English language Translation is attached.